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For Immediate Release

**Cypress 4 Mbit Dual Port Memory Provides Density Upgrade
For High-End Storage Networks, High-Speed WANs
and Wireless Infrastructure Applications**

*High-Density, True Dual-Port, High-Speed RAM is First to Offer Power-Saving 2.5V HSTL I/O,
Provides Simple Upgrade from 1 Mbit*

SAN JOSE, Calif., September 25, 2000 – Cypress Semiconductor (NYSE: CY) today announced the industry's first 4 Mbit, true dual-port memory to offer a power-saving 2.5V high-speed transistor logic (HSTL) I/O, targeted at applications in high-end storage networks, high-speed wide-area networks (WANs) and wireless infrastructure applications. This 4 Mbit specialty memory simplifies its implementation by providing pin compatibility with its 1 Mbit counterpart in the 172-ball grid array (BGA) package – making the device both small and highly manufacturable at a size of 15mm x 15mm with a 1.0 mm pitch – in addition to a TQFP.

The CY7C0852 is configured as a synchronous, dual-ported, 128K x 36 memory, as well as a 256K x 18, 64K x 36, and 128K x 18. It will be offered in both 2.5V HSTL and 3.3V LVTTL I/O versions. The device is capable of transferring 10.8 Gigabits per second at a speed of 150 MHz, easing communications bottlenecks, enabling more efficient backplane management and increasing overall system performance. Its low-power HSTL I/O provides an easy interface to common communications ICs, CPLDs, ASICs, and processors. The device will be manufactured using Cypress's RAM7™, fast transistor, 0.15-micron, CMOS process.

"The 4 Mbit dual-port establishes the upgrade path to higher densities for designers currently using Cypress's 1 Mbit dual-port memories," said Chris Norris, Cypress's vice president, data communications division. "A growing number of designers rely on Cypress as the leader in dual-ported memories due to the technological advantages our devices offer. We delivered a 1 Mbit true dual-ported memory ahead of our competitors, and our new products will lead our customers to higher densities and bandwidths."

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“For our WAN customers, this 4 Mbit dual-port facilitates the prioritization of incoming voice, video and data to facilitate quality of service (QoS) and eliminate head of line (HOL) blocking issues,” added Geoff Charubin, data communications division director of marketing. “Customers typically use the device to store and buffer data in conjunction with CPLDs.”

When integrated into storage networks, the CY7C0852V enhances data manipulation and processing by buffering data from and within fiber channel switches, RAIDs and host bus adapters (HBAs). This enables more efficient backplane management, real-time backup, and reliable data transfer.

The 4 Mbit dual-port shares its target applications – high-performance switching and routing systems – with many other Cypress devices, including other specialty memories like FIFOs and data communications devices such the RoboClock™ and HOTLink™ components. Communications customers that are already familiar with or have used Cypress’s quad-data-rate (QDR), synchronous SRAM, CPLD, and clock solutions are ideal candidates who would benefit from the 4 Mbit dual-port memory.

Availability and Pricing

Part Number	Speed (MHz)	Density	Configuration	Voltage, I/O	Package	Sample/Production
CY7C0852V	100, 150	4 Mbit	128K x 36	3.3V LVTTL	172-ball fBGA	Q101/Q201
CY7C0852V25	100, 150	4 Mbit	128K x 36	2.5V HSTL	172-ball fBGA	Q201/Q301
CY7C0832V	100, 150	4 Mbit	256K x 18	3.3V LVTTL	120-pin TQFP	Q201/Q301
CY7C0851V	100, 150	2 Mbit	64K x 36	3.3V LVTTL	172-ball fBGA	Q101/Q201
CY7C0851V25	100, 150	2 Mbit	64K x 36	2.5V HSTL	172-ball fBGA	Q201/Q301
CY7C0831V	100, 150	2 Mbit	128K x 18	3.3V LVTTL	120-pin TQFP	Q201/Q301

Pricing for the 4 Mbit, 100-MHz device will start at \$95 in quantities of 10,000.

About Cypress

Cypress Semiconductor provides high-performance integrated circuit solutions “By Engineers. For Engineers.”™ for fast-growing companies in fast-growing markets, including data communications, telecommunications, computation, consumer products, and industrial-control. With a focus on emerging communications applications, Cypress's product lines include networking-optimized and micropower static RAMs; high-bandwidth multi-port and FIFO memories; high-density programmable logic devices;

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timing technology for PCs and other digital systems; and controllers for Universal Serial Bus (USB). Cypress is No. 1 in the USB and clock chip markets.

More than two-thirds of Cypress's sales come from fast-growing communications markets and dynamic companies such as Alcatel, Cisco, Ericsson, Lucent, Motorola, Nortel Networks, and 3Com. Cypress's ability to mix and match its broad portfolio of intellectual property enables targeted, integrated solutions for high-speed systems that feed bandwidth-hungry Internet applications. Cypress aims to become the preferred silicon supplier for Internet switching systems and for every Internet data stream to pass through at least one Cypress IC.

Cypress employs more than 4,100 people worldwide with international headquarters in San Jose, California. Its shares are listed on the New York Stock Exchange under the symbol CY. More information about Cypress is accessible electronically on the company's worldwide web site at <http://www.cypress.com> or by CD-ROM (call 1-800-858-1810). An electronic investor forum, and other investor information, is located at <http://www.cypress.com/investor/index.html>.

"Safe Harbor" Statement under the Private Securities Litigation Reform Act of 1995: Statements herein that are not historical facts are "forward-looking statements" involving risks and uncertainties. Please refer to Cypress's Securities and Exchange Commission filings for a discussion of such risks.

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